

L Number	Hits	Search Text	DB	Time stamp
1	56279	INPUT adj port	USPAT;	2004/05/26 16:54
	•		US-PGPUB;	
			EPO;	
.	71700		DERWENT	0004/05/05 16 54
2	71783	output adj port	USPAT;	2004/05/26 16:54
		'	US-PGPUB; EPO;	
			DERWENT	
3	31724	(INPUT adj port) same (output adj port)	USPAT;	2004/05/26 16:54
	31/21	(intot day pore, same (output day pore,	US-PGPUB;	2001,00,20 10.04
			EPO;	
			DERWENT	
4	161	((INPUT adj port) same (output adj port))	USPAT;	2004/05/26 17:08
		same ((semiconductor adj memory) or sram	US-PGPUB;	
]		or sdram)	EPO;	
	201021		DERWENT	0004/05/06 16 56
5	301931	unidirectional or uni-directional or (one	USPAT;	2004/05/26 16:56
		adj direction)	US-PGPUB; EPO;	
			DERWENT	
6	3415	(unidirectional or uni-directional or (one	USPAT;	2004/05/26 16:56
	0.120	adj direction)) with bus	US-PGPUB;	
			EPO;	
			DERWENT	
7	7	(((INPUT adj port) same (output adj port))	USPAT;	2004/05/26 17:07
		same ((semiconductor adj memory) or sram	US-PGPUB;	
		or sdram)) and ((unidirectional or	EPO; DERWENT	
		uni-directional or (one adj direction)) with bus)	DEKWENI	
8	6044	plurality with (INPUT adj port)	USPAT;	2004/05/26 17:07
	0011	praration with (amon any porce,	US-PGPUB;	2001,00,20 2,101
			EPO;	
			DERWENT	
9	4060	(plurality with (INPUT adj port)) same	USPAT;	2004/05/26 17:07
		(output adj port)	US-PGPUB;	
			EPO;	
10	109098	((semiconductor adj memory) or sram or	DERWENT USPAT;	2004/05/26 17:08
10	109090	sdram)	US-PGPUB;	2004/03/20 17.08
) Salam,	EPO;	
			DERWENT	
11	250	((plurality with (INPUT adj port)) same	USPAT;	2004/05/26 17:08
		(output adj port)) and (((semiconductor	US-PGPUB;	
		adj memory) or sram or sdram))	EPO;	
12 .	_	///mlumplitureith /TXIDIIM = 34 march) ==	DERWENT	2004/05/20 13 00
12	7	(((plurality with (INPUT adj port)) same (output adj port)) and (((semiconductor	USPAT; US-PGPUB;	2004/05/26 17:08
		adj memory) or sram or sdram))) and	EPO;	
		((unidirectional or uni-directional or	DERWENT	
].		(one adj direction)) with bus)		ē
13	5	((((plurality with (INPUT adj port)) same	USPAT;	2004/05/26 17:14
		(output adj port)) and (((semiconductor	US-PGPUB;	
		adj memory) or sram or sdram))) and	EPO;	
		((unidirectional or uni-directional or	DERWENT	
		(((((INPUT adj port) same (output adj		
		port) same ((semiconductor adj memory) or		
		sram or sdram)) and ((unidirectional or		
		uni-directional or (one adj direction))		
		with bus))		
14	569	((plurality with (INPUT adj port)) same	USPAT;	2004/05/26 17:14
]		(output adj port)) same memory	US-PGPUB;	
			EPO;	
15	12	(((plurality with (INPUT adj port)) same	DERWENT USPAT;	2004/05/26 17:15
1	12	((thrutality with (invol adj port)) same (output adj port)) same memory) and	US-PGPUB;	2004/03/20 17:13
		((unidirectional or uni-directional or	EPO;	
		(one adj direction)) with bus)	DERWENT	



				1
16	9	((((plurality with (INPUT adj port)) same	USPAT;	2004/05/26 17:15
		(output adj port)) same memory) and	US-PGPUB;	
		((unidirectional or uni-directional or	EPO;	
1		(one adj direction)) with bus)) not	DERWENT	
		(((((plurality with (INPUT adj port)) same		
		(output adj port)) and (((semiconductor		
			1	
		adj memory) or sram or sdram))) and		
		((unidirectional or uni-directional or		
,		(one adj direction)) with bus)) not		
		((((INPUT adj port) same (output adj		1
	<u> </u>	port)) same ((semiconductor adj memory) or		İ
		sram or sdram)) and ((unidirectional or		
		uni-directional or (one adj direction))		
	l	with bus)))		
17	7	(((((plurality with (INPUT adj port)) same	·USPAT;	2004/05/26 17:32
		(output adj port)) same memory) and	US-PGPUB;	
		((unidirectional or uni-directional or	EPO;	
		(one adj direction)) with bus)) not	DERWENT	
			DEKWENI	ŀ
		(((((plurality with (INPUT adj port)) same		
1		(output adj port)) and (((semiconductor		
		adj memory) or sram or sdram))) and		
		((unidirectional or uni-directional or		
		(one adj direction)) with bus)) not		
		((((INPUT adj port) same (output adj		
		port)) same ((semiconductor adj memory) or		
	! 	sram or sdram)) and ((unidirectional or	1]
		uni-directional or (one adj direction))		!
		with bus)))) not ((((INPUT adj port) same		1
		(output adj port)) same ((semiconductor		
		adj memory) or sram or sdram)) and		
		((unidirectional or uni-directional or		
		(one adj direction)) with bus))		
18	3056	standard adj mode	USPAT;	2004/05/26 17:32
			US-PGPUB;	
			EPO;	
1			DERWENT	·
19	24250	address adj register	USPAT;	2004/05/26 17:33
19	24230	address adj register	,	2004/03/20 17:33
1			US-PGPUB;	
			EPO;	
			DERWENT	
20	28884	data adj register	USPAT;	2004/05/26 17:33
			US-PGPUB;	
		•	EPO;	
			,	
			DERWENT	0004/05/55 55 55
21	91	(standard adj mode) and (address adj	USPAT;	2004/05/26 17:33
		register) and (data adj register)	US-PGPUB;	1
			EPO;	
			DERWENT	
22	28463	16-bit or 16bit	USPAT;	2004/05/26 17:34
~~	20403	TO DIE OF TODIE		2004,03/20 17.34
			US-PGPUB;	
			EPO;	
			DERWENT	
23	77	((standard adj mode) and (address adj	USPAT;	2004/05/26 17:34
		register) and (data adj register)) and	US-PGPUB;	
		(16-bit or 16bit)	EPO;	
		(10 210 01 10210)	DERWENT .	
1 04	0005	20 144 2014		0004/05/05 15 5:
24	22854	32-bit or 32bit	USPAT;	2004/05/26 17:34
			US-PGPUB;	1
			EPO;	1
			DERWENT	
25	72	(((standard adj mode) and (address adj	USPAT;	2004/05/26 17:35
	'2	, , , , , , , , , , , , , , , , , , , ,		2004/03/20 17.33
		register) and (data adj register)) and	US-PGPUB;	
		(16-bit or 16bit)) and (32-bit or 32bit)	EPO;	.
			DERWENT	
				· · · · · · · · · · · · · · · · · · ·